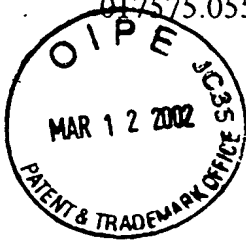


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PATENT APPLICATION
SERIAL NO. 09/997,786

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E. Willis
4-29-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Duncan M. Walker, et al.
Serial No.: 09/997,786
Filing Date: November 30, 2001
Title: SYSTEM AND METHOD FOR DETECTING
QUIESCENT CURRENT IN AN INTEGRATED
CIRCUIT
Confirmation No: 4798

Honorable Assistant Commissioner
For Patents
Washington, D.C. 20231

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ORIGINALLY FILED

Dear Sir:

PRELIMINARY AMENDMENT

Prior to the initial review of this patent application entitled *SYSTEM AND METHOD FOR DETECTING QUIESCENT CURRENT IN AN INTEGRATED CIRCUIT* by Duncan M. Walker et al., please amend the application as follows: